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Nataraj et al.(10) Pub. No.: **US 2002/0161969 A1**(43) Pub. Date: **Oct. 31, 2002**(54) **CONTENT ADDRESSABLE MEMORY WITH
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(52) U.S. Cl. **711/108; 711/158**(21) Appl. No.: **10/000,122**(22) Filed: **Oct. 31, 2001****Related U.S. Application Data**(63) Continuation-in-part of application No. 09/406,170,
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in-part of application No. 09/815,778, filed on Mar.
24, 2001. Continuation-in-part of application No.
09/940,832, filed on Aug. 27, 2001.(57) **ABSTRACT**

A content addressable memory (CAM) device including a CAM array and a priority index table. The CAM array has a plurality of rows of CAM cells, each row including a plurality of row segments and being adapted to store a data word that spans a selectable number of the row segments. The priority index table is coupled to the plurality of rows of CAM cells and is adapted to store a plurality of priority numbers, each priority number being indicative of a priority of a corresponding data word stored in the CAM array.

